

DELAY LINE

ABSTRACT OF THE DISCLOSURE

A delay line comprising a dielectric substrate including a pair of main surfaces; a transmission line disposed on one of the main surfaces of the dielectric substrate; a ground conductor disposed on the other of the main surfaces of the dielectric substrate; and at least one of a variable capacitor and a diode being disposed on the dielectric substrate and connected in parallel to the transmission line for setting a desired delay time of the delay line. In the above delay line, the delay time can be adjusted even after the delay line is mounted on a printed circuit board, and further, the delay time can be continuously adjusted. The delay line can also be formed in a multilayer structure rather than on the above dielectric substrate.